

Features	I.MX6UL	I.MX28	I.MX35
CPU	ARM Cortex A7	ARM9 Architecture	ARM11 Architecture
Maximum clock speed	528MHz	454 MHz	532 MHz
I-Cache/D-Cache	32 KB / 32 KB L1, 128 KB L2	16 KB / 32KB	16 KB / 16KB
Embedded SRAM	128 Kb	128 Kb	128 KB
External Memory Interface and DDR Bus Speed	X16 LP-DDR2, DDR3/LV-DDR3 at 400 MHz	LPDDR, DDR2, LV-DDR2, NORvia SPI, SLC/MLC, Managed NAND	LPDDR, DDR2, SDRAM, LPDDR, NOR, SLC/MLC NAND
LCD Resolution	WXGA(1366*768)	Up to WAGA (860*480)	Up to SVGA (800*600)
Touch Screen Controller	yes	yes	no
Hardware Video Acceleration	SW only	no	no
Hardware 2D/3D Graphics Acceleration	no	no	OpenVG
CMOS Sensor Interface	1	no	1
UART	8	6	3
SPI/I2C	4 / 4	4 / 2	2 / 3
USB Controller	2* HS USB 2.0 OTG	HS Host/Device, HS Host	HS OTG, HS Host
USB PHY	2	2	2
Power Management	Freescall MMPF3000/Discrete	integrated	MC13892
Digital Audio Interface	SAI / I2S*3	SSI / I2S	SSI / I2S, ESAI
Ethernet	2*10/100 mb/s + IEEE 1588	2 * 10/100 + L2 Switch	10 /100
PCI Express*	no	no	no
CAN	2	2	2
Multimedia card/secure digital controller	2	4	2
Hard disk Drive Interface	no	no	PATA
Smart card interface module	yes	no	no
Security	Secure Boot, RNG, Tamper Detection, Secure Storage, AES-128, DES 3DES, ARC4, MD5, SHA-1, SHA-224, SHA-256, 16 KB Secure RAM, Tamper Resistent RTC, Secure Debug, OTP Space, OTF Encryption/Decryption	Secure Boot, Key Storage, AES, SHA-256, PRNG	Secure Boot, Tamper Reaction, RNG, Key Storage, Secure Debug
Timer	4	4	3
RTC	Secure RTC	1	1
PWM	8	8	1
Package	14x14 289 MAPBGA 0.8 mm pitch 9x9 289 MAPBGA 0.5 mm pitch	0.8 mm,289 MAPBGA	17 * 17 MAPBGA 0.8 mm
ADC Channels	Yes, Two 12-bit ADC, 1 with touch controller, up to 10 channel	8	no
Qualifications	Automotive,Industrial and commercial	Automotive,industrial and commercial	Automotive,industrial and commercial